

FIGURE 1



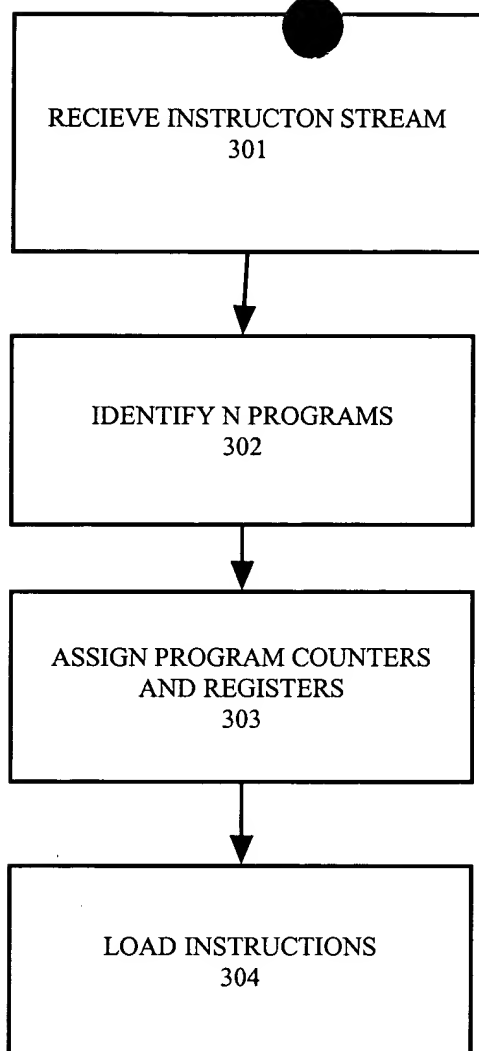
[illegible]

FIGURE 3

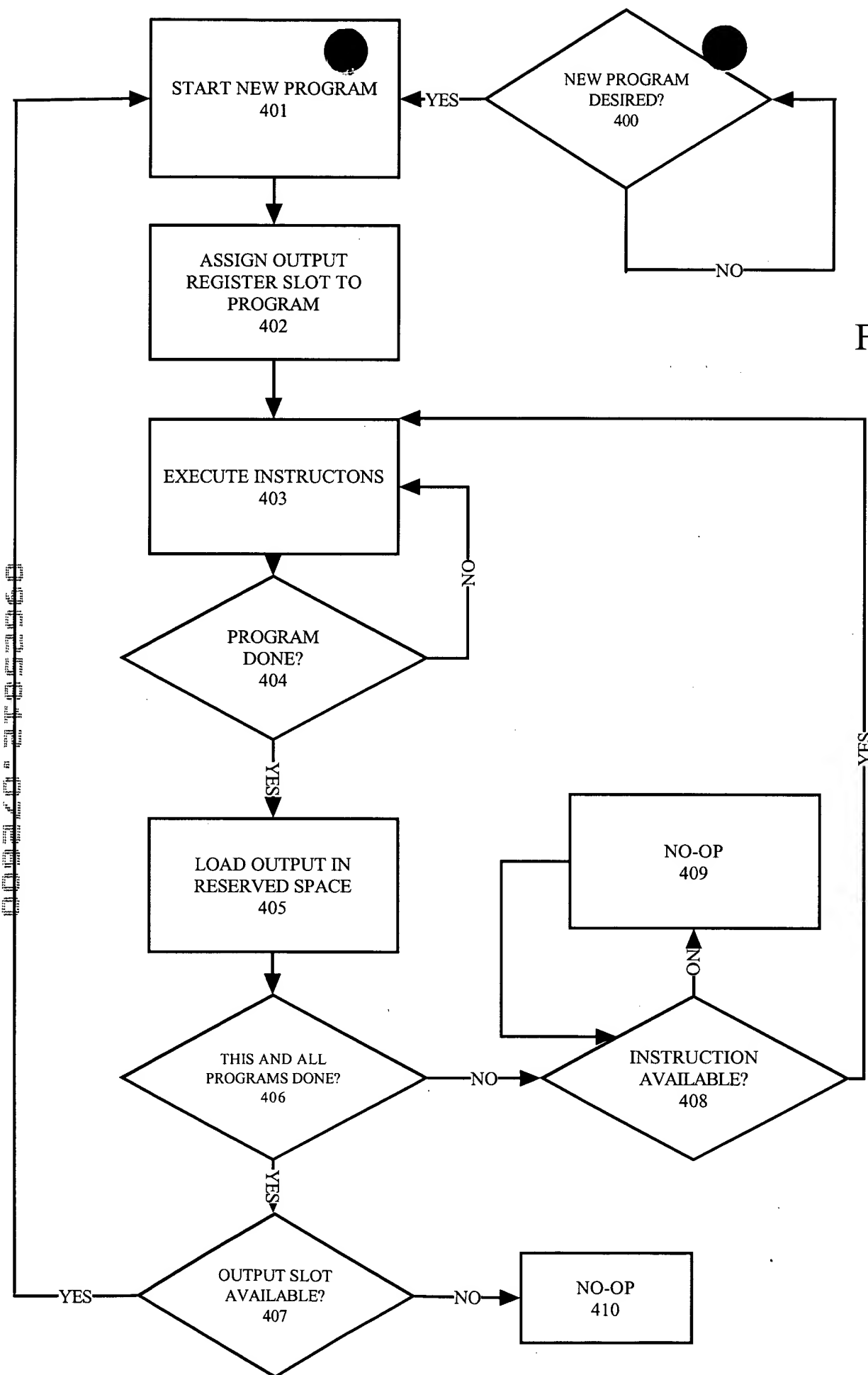


FIGURE 4

4 BY 32b VECTOR REGISTER

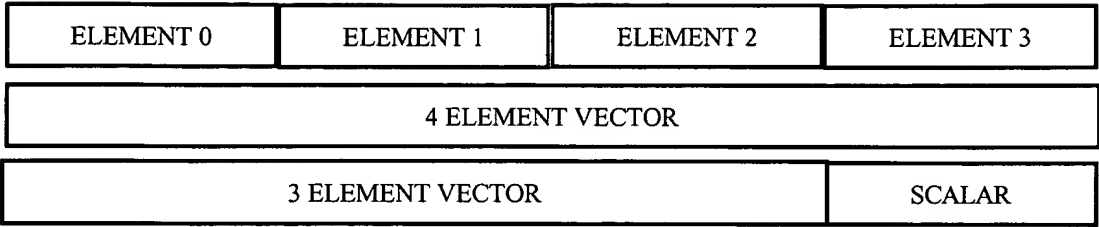


FIGURE 5

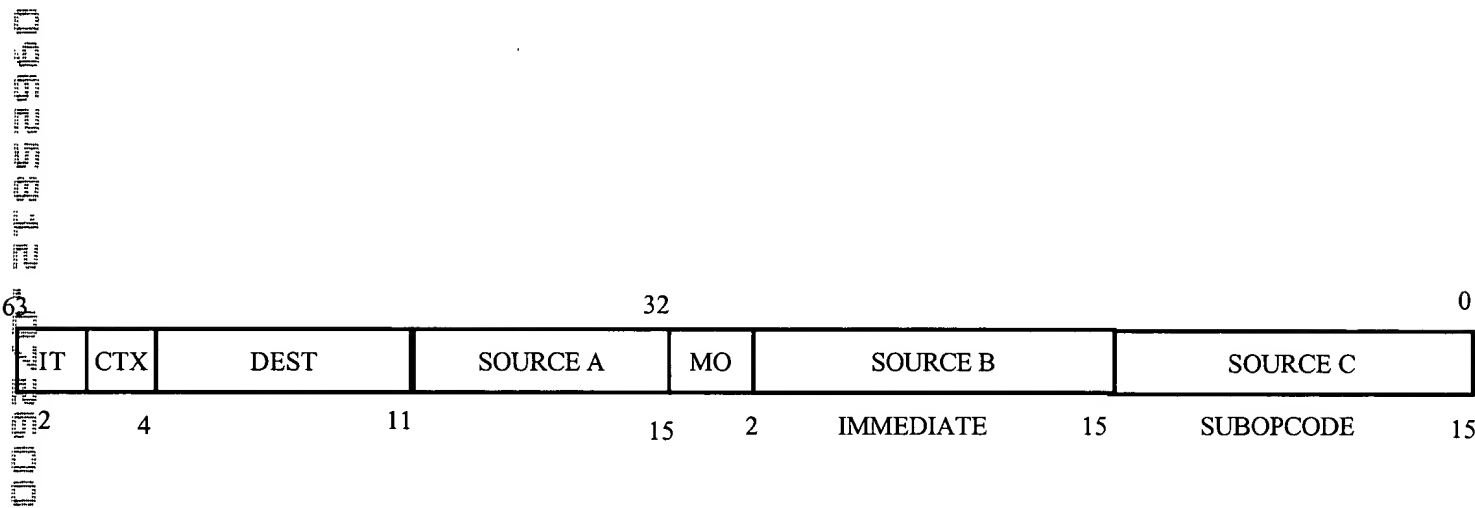


FIGURE 6